

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1-6. (Canceled).

7. (Currently Amended) The device as recited in Claim ~~[[6]]~~ 10, wherein the device is for data to be transported via a network bridge.

8. (Currently Amended) A ~~[[The]]~~ device ~~as recited in Claim 7~~ for controlling a memory and for data to be transported via a network bridge, further comprising:

a memory that is subdivided into a plurality of memory areas that are configurable independently of each other;

an arrangement for preadjusting the memory areas, and for performing a dynamic modification with respect to at least one of a size and a number of the memory areas, during operation;

an arrangement for configuring and controlling the network bridge so as to query data, analyze data, and obtain parameters for operating the memory on the basis of the analysis; and

an interface via which the memory is connected ~~via an interface~~ to the arrangement for configuring and controlling ~~[[means]]~~.

9. (Previously Presented) The device as recited in Claim 8, wherein the analysis is with respect to a memory division and occupancy by different types of data in the memory.

10. (Currently Amended) A [[The]] device as recited in Claim 6 for controlling a memory, further comprising:

a memory that is subdivided into a plurality of memory areas that are configurable independently of each other;

an arrangement for preadjusting the memory areas, and for performing a dynamic modification with respect to at least one of a size and a number of the memory areas, during operation; and

an interface via which the memory is able to be connected to an external memory in order to thereby increase an overall size of the memory.

11. (Previously Presented) The device as recited in Claim 8, further comprising:
at least one additional interface via which the arrangement for configuring and controlling is able to be connected to additional functional blocks of the network bridge, in order to collect and analyze data, and in order to modify parameters within the additional functional blocks, including the memory, as a function thereof.

12. (Previously Presented) The device as recited in Claim 8, wherein the arrangement for configuring and controlling includes a software layer within a network bridge architecture.

13. (New) The device as recited in Claim 8, further comprising:
an interface via which the memory is able to be connected to an external memory in order to thereby increase an overall size of the memory.

14. (New) A device for controlling a memory and for data to be transported via a network bridge, comprising:

a memory that includes a plurality of individual memories that are configurable independently of each other;

an arrangement for preadjusting the individual memories, and for performing a dynamic modification with respect to at least one of a size and a number of the individual memories, during operation;

an arrangement for configuring and controlling the network bridge so as to query data, analyze data, and obtain parameters for operating the memory on the basis of the analysis; and

an interface via which the memory is connected to the arrangement for configuring and controlling.

15. (New) The device as recited in Claim 14, further comprising:

an interface via which the memory is able to be connected to an external memory in order to thereby increase an overall size of the memory.

16. (New) The device as recited in Claim 14, wherein the analysis is with respect to a memory division and occupancy by different types of data in the memory.

17. (New) The device as recited in Claim 14, wherein the arrangement for configuring and controlling includes a software layer within a network bridge architecture.

18. (New) The device as recited in Claim 14, further comprising:

at least one additional interface via which the arrangement for configuring and controlling is able to be connected to additional functional blocks of the network bridge, in order to collect and analyze data, and in order to modify parameters within the additional functional blocks, including the memory, as a function thereof.

19. (New) A device for controlling a memory and for data to be transported via a network bridge, comprising:

a memory that includes a plurality of individual memories that are configurable independently of each other;

an arrangement for preadjusting the individual memories, and for performing a dynamic modification with respect to at least one of a size and a number of the individual memories, during operation; and

an interface via which the memory is able to be connected to an external memory in order to thereby increase an overall size of the memory.

20. (Currently Amended) The device as recited in Claim 19, wherein the device is for data to be transported via a network bridge.